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L17 AND task	256

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L18

Search History

DATE: Thursday, May 10, 2007

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<u>L18</u>	L17 AND task	256	<u>L18</u>
<u>L17</u>	L15 and (swap or swapper)	336	<u>L17</u>
<u>L16</u>	L15 and interrupt	3	<u>L16</u>
<u>L15</u>	L14 and (optimize or optimizer)	3148	<u>L15</u>
<u>L14</u>	(operating adj system) and threshold	16698	<u>L14</u>
<u>L13</u>	page ADJ fault	1275	<u>L13</u>
<u>L12</u>	(717/154 717/128).ccls. or (718/103 718/108).ccls. or (711/128).ccls. or (714/47).ccls.	2777	<u>L12</u>
<u>L11</u>	(717/151 717/128).ccls. or (718/103 718/108).ccls. or (711/128).ccls. or (714/47).ccls.	2850	<u>L11</u>
<u>L10</u>	L9 not L8	8	<u>L10</u>
<u>L9</u>	L6 and (threshold).ab.	9	<u>L9</u>
<u>L8</u>	L6 and (cycles ADJ per ADJ instruction)	12	<u>L8</u>

<u>L7</u>	L6 and (cycles ADJ per ADJ instruction ADJ counter)	0	<u>L7</u>
<u>L6</u>	L5 and (optimizer or optimize or optimization)	172	<u>L6</u>
<u>L5</u>	L4 and threshold	1084	<u>L5</u>
<u>L4</u>	L3 OR L2	4479	<u>L4</u>
<u>L3</u>	cycle ADJ counter	3737	<u>L3</u>
<u>L2</u>	instruction ADJ counter	784	<u>L2</u>
<u>L1</u>	hardware ADJ performance	760	<u>L1</u>

END OF SEARCH HISTORY